

FIG. 1

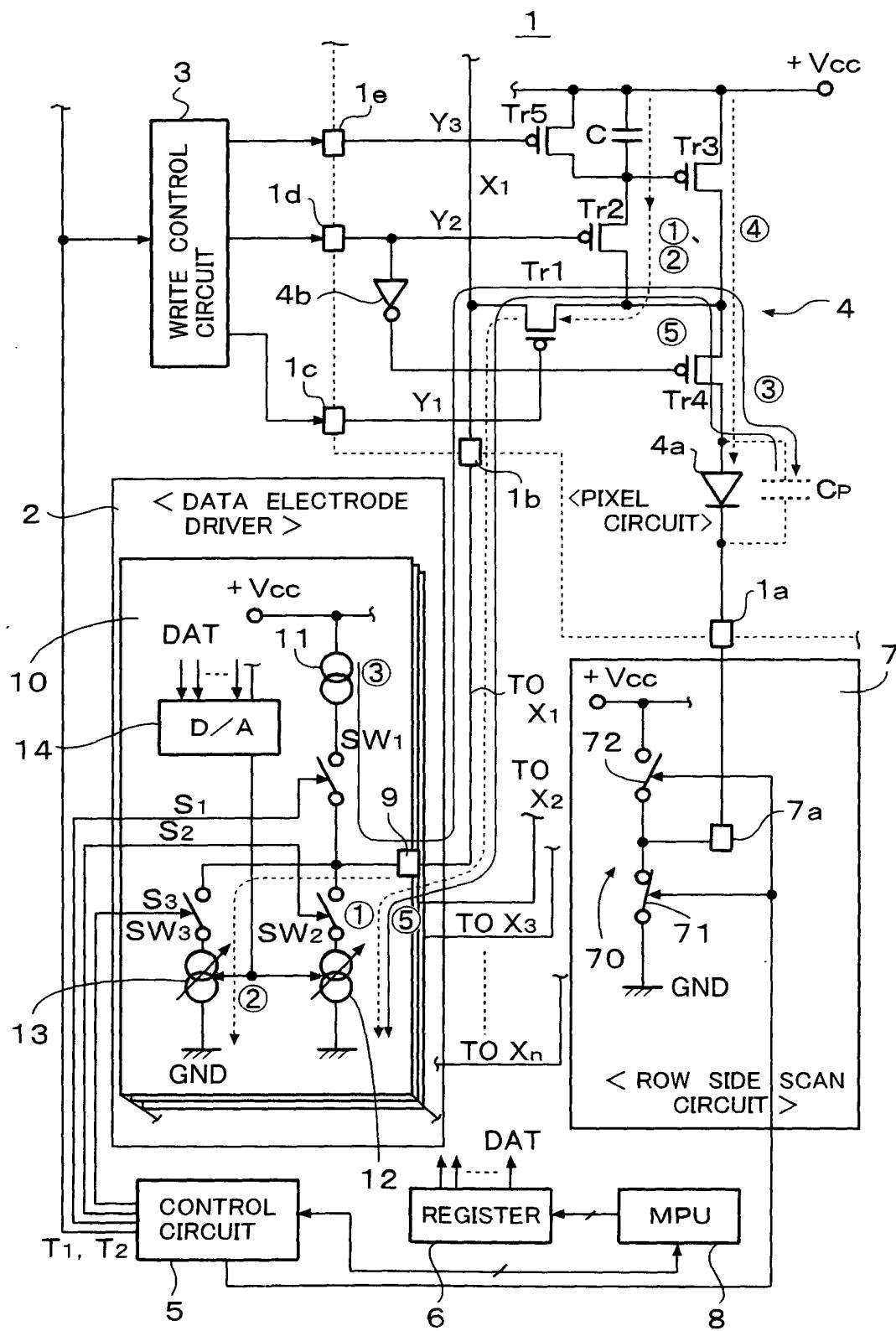


FIG. 2

	SELECTION LINE, ERASE LINE		CONTROL SIGNAL		TRANSISTORS OF PIXEL CIRCUIT				CURRENT PATH
	Y ₁	Y ₂	Y ₃	S ₁	S ₂	S ₃	Tr ₁	Tr ₂	Tr ₃ Tr ₄ Tr ₅
INITIAL CHARGE OF CAPACITOR C	"L"	"L"	"H"	"L"	"H"	"H"	ON	ON	ON OFF OFF
VOLTAGE WRITE IN CAPACITOR C	"L"	"L"	"H"	"L"	"L"	"H"	ON	ON	ON OFF OFF
INITIAL CHARGE OF OEL ELEMENT	"L"	"H"	"H"	"H"	"L"	"L"	ON	OFF	ON ON OFF
LIGHT EMITTING DRIVE OF OEL ELEMENT	"H"	"H"	"H"	"L"	"L"	"L"	OFF	OFF	ON ON OFF
RESETTING OF CAPACITOR C AND OEL ELEMENT	"L"	"H"	"L"	"L"	"H"	"L"	ON	OFF	OFF ON ON

FIG. 3

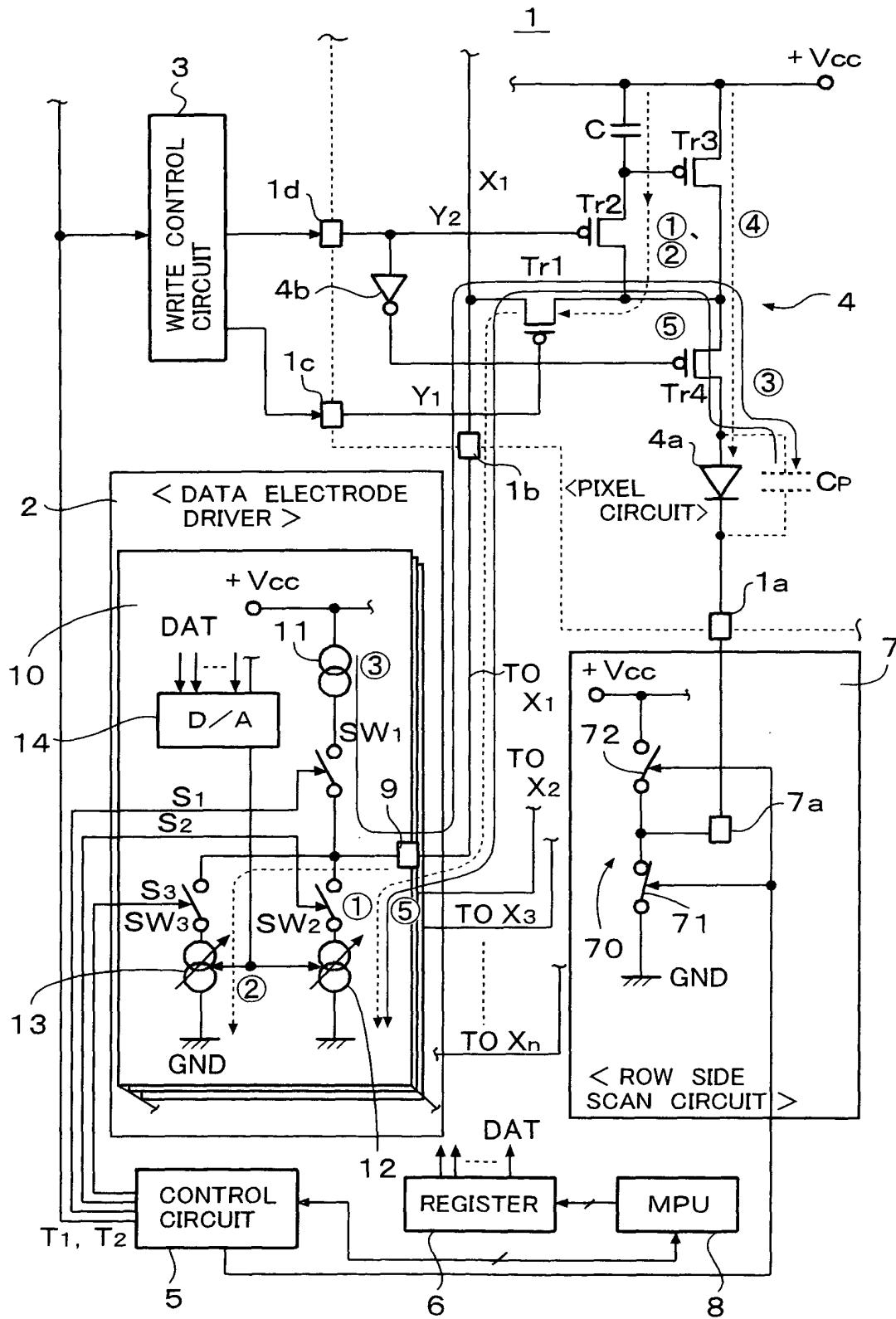


FIG. 4

